

# **HA13164AH**

## Multiple Voltage Regulator for Car Audio

REJ03F0139-0200 Rev.2.00 Jan 16, 2007

#### **Description**

The HA13164AH is a compact multiple voltage regulator for car audio system. The outputs of this IC output consist of regulated 5.7 V output for a microcontroller, regulated 8 V output for CD driver, regulated 9.0 V output for audio control, regulated 10 V output for illuminations and regulated 5 V output, VCC-dependent output for external output and VCC-dependent output for remote-ANT.

#### **Functions**

#### General

- ACC power monitor circuit is built-in as to detect low voltage.
- Low saturation output (PNP output) used for audio output.
- Adjustable voltage for illumination output by changing an external resistor.

#### **Protections**

- Output current limit circuit to avoid device destruction caused by shorted output, etc.
- High surge input protector against VCC and ACC.
- Built in a thermal shutdown circuit to prevent against the thermal destruction.

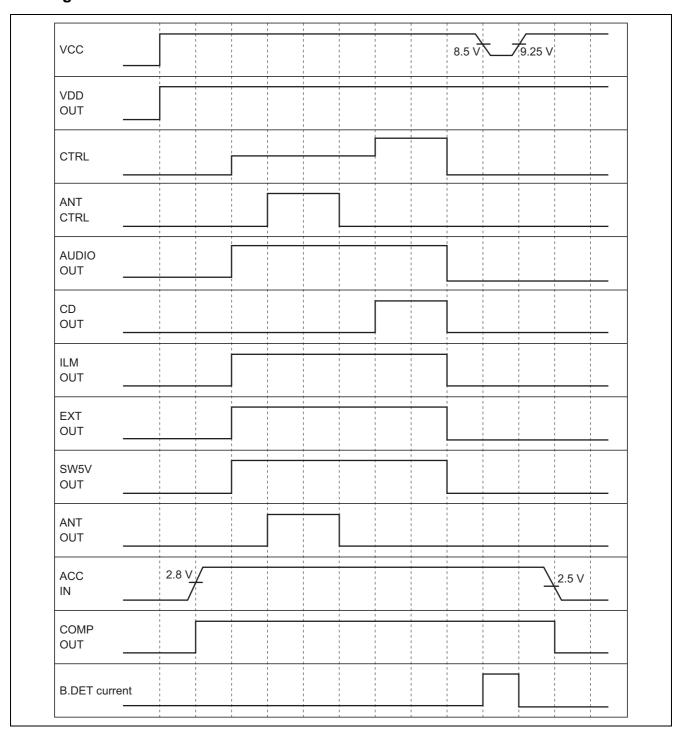
# Pin Description and Equivalent Circuit

|     |          |                    |                               | Function  |       |       |
|-----|----------|--------------------|-------------------------------|---|-------|-------|
| Pin |          |                    |                               |   |       | Surge |
| No. | Pin Name | Specification      | Equivalent Circuit            | Normal Operation  | TSD   | Input |
| 1   | EXT OUT  | VCC-1 V/300 mA min | Vcc -Vcc                      | Output voltage is VCC-1 V when M or H level applied to CTRL pin.                          | 0 V   | 0 V   |
| 2   | ANT OUT  | VCC-1 V/300 mA min | \$90 kΩ<br>\$10 kΩ            | Output voltage is VCC-1<br>V when M or H level to<br>CTRL pin and H level to<br>ANT-CTRL. | 0 V   | 0 V   |
| 3   | ACC IN   | _                  | 45 kΩ<br>                     | Connected to ACC.   | _     | _     |
| 4   | VDD OUT  | 5.7 V/100 mA min   | Vcc<br>Vcc<br>175 kΩ<br>50 kΩ | Regular 5.7 V.  | 5.7 V | 0 V   |
| 5   | SW5V OUT | 5.0 V/100 mA min   | VDD                           | Output voltage is 5 V when M or H level applied to CTRL pin.                              | 0 V   | 0 V   |
| 6   | COMP OUT | 5.0 V/100 mA min   | ≶50 kΩ                        | Output for ACC detector   | 0 V   | 0 V   |
| 7   | ANT CTRL | _                  | 51 kΩ<br>                     | L: ANT output OFF<br>H: ANT output ON   |       |       |
| 8   | VCC      | —                  |                               | Connected to VCC  | _     | _     |

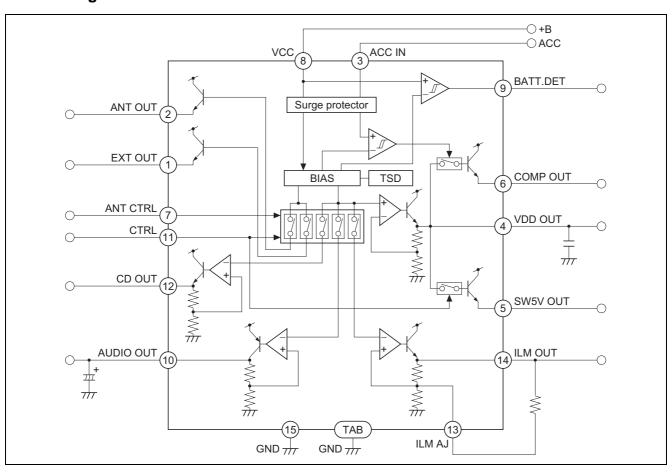
# Pin Description and Equivalent Circuit (cont.)

|            |           |                   |                                      | Function   |        |                |
|------------|-----------|-------------------|--------------------------------------|--|--------|----------------|
| Pin<br>No. | Pin Name  | Specification     | Equivalent Circuit                   | Normal Operation   | TSD    | Surge<br>Input |
| 9          | BATT DET  | _                 | VDD<br>250 kΩ 10 kΩ                  | Low battery detect.  | Detect | Not<br>detect  |
| 10         | AUDIO OUT | 9.0 V/500 mA min  | 77.3 kΩ                              | Output voltage is 9 V when M or H level applied to CTRL pin. | 0 V    | 0 V            |
| 11         | CTRL      | _                 | 65 kΩ<br>35 kΩ                       | L: BIAS OFF<br>M: BIAS ON<br>H: CD ON                        | _      | _              |
| 12         | CD OUT    | 8.0 V/1.3 A min   | Vcc<br>Vcc<br>\$64.7 kΩ<br>\$12.4 kΩ | Output voltage is 8 V when H level applied to CTRL pin.      | 0 V    | 0 V            |
| 13         | ILM AJ    | _                 | → Vcc<br>→ Vcc                       | Adjustment pin for ILM output voltage.                       | _      | _              |
| 14         | ILM OUT   | 9.85 V/500 mA min | ₹33.4 kΩ<br>₹5 kΩ                    | Output voltage is 10 V when M or H level applied to CTRL pin | 0 V    | 0 V            |
| 15         | GND       | _                 | ///                                  | Connected to GND   | _      | _              |

## **Timing Chart**



## **Block Diagram**



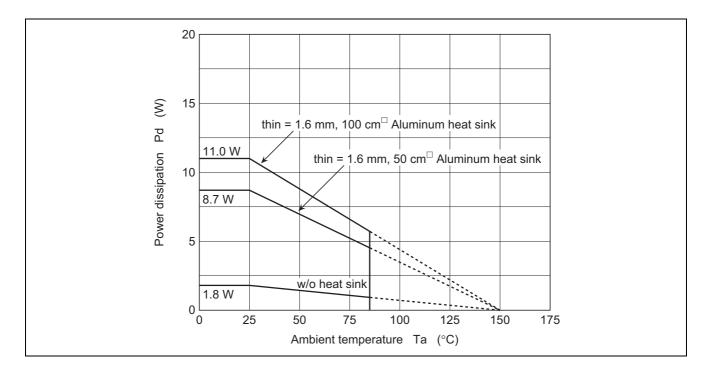
### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

| Item                           | Symbol    | Rating      | Unit | Note |
|--------------------------------|-----------|-------------|------|------|
| Operating power supply voltage | Vcc       | 18          | V    |      |
| DC supply voltage              | Vcc(DC)   | 26          | V    | 1    |
| Peak voltage                   | Vcc(PEAK) | 50          | V    | 2    |
| Power dissipation              | Pd        | 36          | W    | 3    |
| Junction temperature           | Tj        | 150         | °C   |      |
| Operating temperature          | Topr      | -40 to +85  | °C   |      |
| Storage temperature            | Tstg      | -55 to +125 | °C   |      |

Notes: Recommended power supply voltage range 10 to 16 V.

- 1. Applied time is less than 30 s.
- 2. Surge pulse as input.
- 3. Ta = 25°C.: Permissible power dissipation when using a heat sink of infinite area. Refer to the derating curves below.

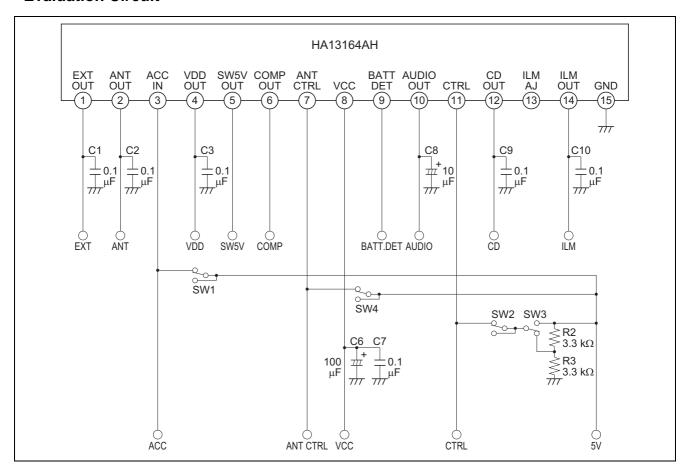


## **Electrical Characteristics**

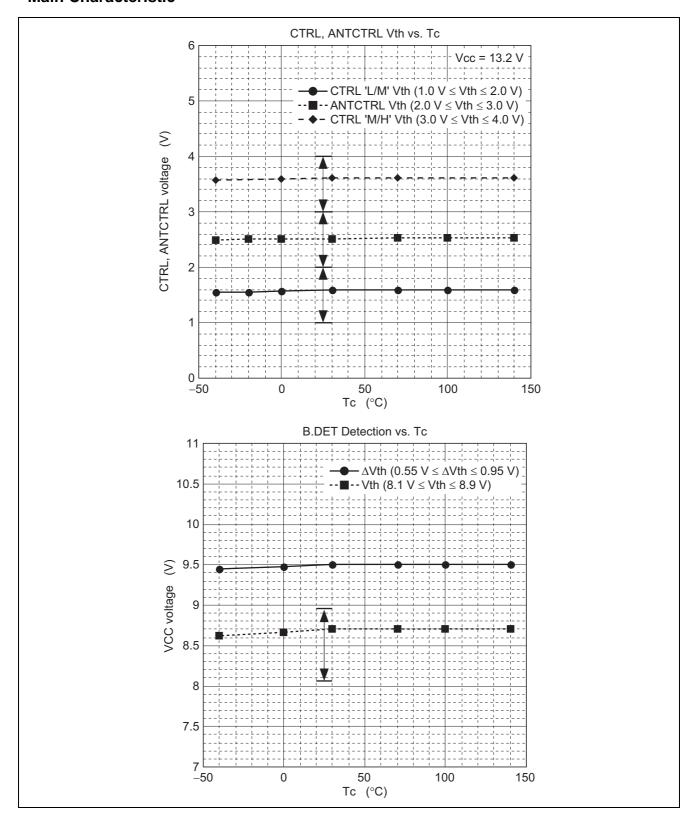
(unless otherwise noted, Vcc = 13.2 V,  $Ta = 25^{\circ}\text{C}$ )

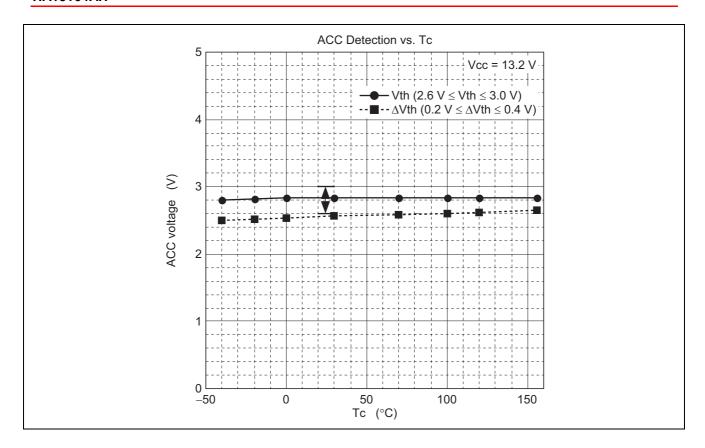
| Item                       |                                  | Symbol | Min  | Тур  | Max   | Unit | Test Condition                   |
|----------------------------|----------------------------------|--------|------|------|-------|------|----------------------------------|
| Standby current            |                                  | IST    | _    | 460  | 700   | μА   | ACC = 0 V, CTRL = 0 V            |
| CTRL L level (STBY mode)   |                                  | VCL    | 0    | _    | 1.0   | V    |                                  |
| CTRL M level (CD OFF mode) |                                  | VCM    | 2.0  | _    | 3.0   | V    |                                  |
| CTRL H level (CD ON mode)  |                                  | VCH    | 4.0  | _    | _     | V    |                                  |
| ANT CT                     | ANT CTRL L level (ANT OFF mode)  |        | 0    | _    | 2.0   | V    |                                  |
| ANT CT                     | RL H level (ANT ON mode)         | VACH   | 3.0  | _    | _     | V    |                                  |
| VDD                        | Output voltage                   | Vo1    | 5.4  | 5.7  | 6.0   | V    | Io1 = 80 mA                      |
| OUT                        | Voltage regulation               | ∆Vo11  | _    | 10   | 50    | mV   | Vcc = 10 to 16 V, lo1 = 80 mA    |
|                            | Load regulation                  | ΔVo12  | _    | 50   | 100   | mV   | Io1 = 0 to 80 mA                 |
|                            | Minimum I/O voltage differential | ΔVo13  | _    | 1.0  | 1.5   | V    | Io1 = 80 mA                      |
|                            | Output current capacity          | lo1    | 100  | 250  | _     | mA   | Vo1 ≥ 5.4 V                      |
|                            | Ripple rejection ratio           | SVR1   | 50   | 60   | _     | dB   | f = 100 Hz, Io1 = 80 mA          |
| CD                         | Output voltage                   | Vo2    | 7.6  | 8.0  | 8.4   | V    | lo2 = 1.0 A                      |
| OUT                        | Voltage regulation               | ∆Vo21  | _    | 40   | 100   | mV   | Vcc = 10 to 16V, lo2 = 1.0 A     |
|                            | Load regulation                  | ΔVo22  | _    | 70   | 150   | mV   | lo2 = 10m to 1.0 A               |
|                            | Minimum I/O voltage differential | ΔVo23  | _    | 1.0  | 1.5   | V    | lo2 = 1.0 A                      |
|                            | Output current capacity          | lo2    | 1.3  | 2.0  | _     | Α    | Vo2 ≥ 7.6 V                      |
|                            | Ripple rejection ratio           | SVR2   | 40   | 45   | _     | dB   | f = 100 Hz, lo2 = 1.0 A          |
| AUDIO                      | Output voltage                   | Vo3    | 8.5  | 9.0  | 9.5   | V    | Io3 = 400 mA                     |
| OUT                        | Voltage regulation               | ΔVo31  | _    | 30   | 90    | mV   | Vcc = 10 to 16 V, lo3 = 400 mA   |
|                            | Load regulation                  | ΔVo32  | _    | 100  | 200   | mV   | Io3 = 10 to 400 mA               |
|                            | Minimum I/O voltage differential | ΔVo33  | _    | 0.4  | 0.9   | V    | Io3 = 400 mA                     |
|                            | Output current capacity          | lo3    | 500  | 850  |       | mA   | Vo3 ≥ 8.5 V                      |
|                            | Ripple rejection ratio           | SVR3   | 45   | 50   | _     | dB   | f = 100 Hz, Io3 = 400 mA         |
| ILM                        | Output voltage                   | Vo4    | 9.35 | 9.85 | 10.35 | V    | Io4 = 400 mA                     |
| OUT                        | Voltage regulation               | ΔVo41  | _    | 40   | 100   | mV   | Vcc = 12.5 to 16 V, lo4 = 400 mA |
|                            | Load regulation                  | ΔV042  | _    | 50   | 100   | mV   | Io4 = 10 to 400 mA               |
|                            | Minimum I/O voltage differential | ΔVo43  | _    | 1.0  | 1.5   | V    | Io4 = 400 mA                     |
|                            | Output current capacity          | lo4    | 500  | 900  | _     | mA   | Vo4 ≥ 9.35 V                     |
|                            | Ripple rejection ratio           | SVR4   | 35   | 40   | _     | dB   | f = 100 Hz, Io4 = 400 mA         |
| EXT                        | Differential I/O voltage         | ∆Vo51  | _    | 1.0  | 1.5   | V    | Io5 = 300 mA                     |
| OUT                        | Load regulation                  | ∆Vo52  | _    | 350  | 600   | mV   | lo5 = 10 to 300 mA               |
|                            | Output current capacity          | lo5    | 300  | 500  | _     | mA   | Vo5 ≥ 11.7 V                     |
| ANT                        | Differential I/O voltage         | ΔVo61  | _    | 1.0  | 1.5   | V    | Io6 = 300 mA                     |
| OUT                        | Load regulation                  | ΔV062  | _    | 350  | 600   | mV   | lo6 = 10 to 300 mA               |
|                            | Output current capacity          | lo6    | 300  | 500  | —     | mA   | Vo6 ≥ 11.7 V                     |
| SW5V                       | Output voltage                   | Vo7    | 4.6  | 5.0  | 5.4   | V    | Io7 = 80 mA, VDD = no load       |
| OUT                        | Output current capacity          | lo7    | 100  | 300  |       | mA   | Vo7 ≥ 4.6 V                      |
| ACC<br>OUT                 | Output voltage                   | Vo8    | 4.6  | 5.0  | 5.4   | V    | lo8 = 40 mA, VDD = no load       |
|                            | Output current capacity          | lo8    | 100  | 300  | _     | mA   | Vo8 ≥ 4.6 V                      |
|                            | Rise threshold voltage           | VTHH8  | 2.6  | 2.8  | 3.0   | V    |                                  |
|                            | Hysteresis range                 | ΔVTH8  | 0.2  | 0.3  | 0.4   | V    |                                  |
| BATT.                      | Threshold voltage                | VTHH9  | 8.1  | 8.5  | 8.9   | V    |                                  |
| DET                        | Hysteresis range                 | ΔVTH9  | 0.55 | 0.75 | 0.95  | V    |                                  |
|                            | Output current capacity          | lo9    | 200  | _    | _     | μА   | Vo = 0.3 V                       |

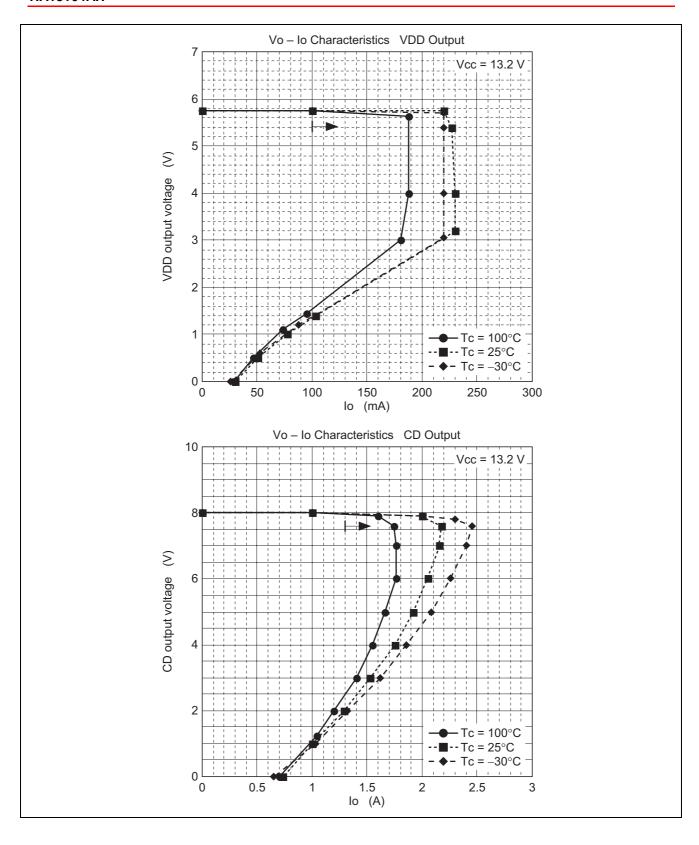
#### **Evaluation Circuit**

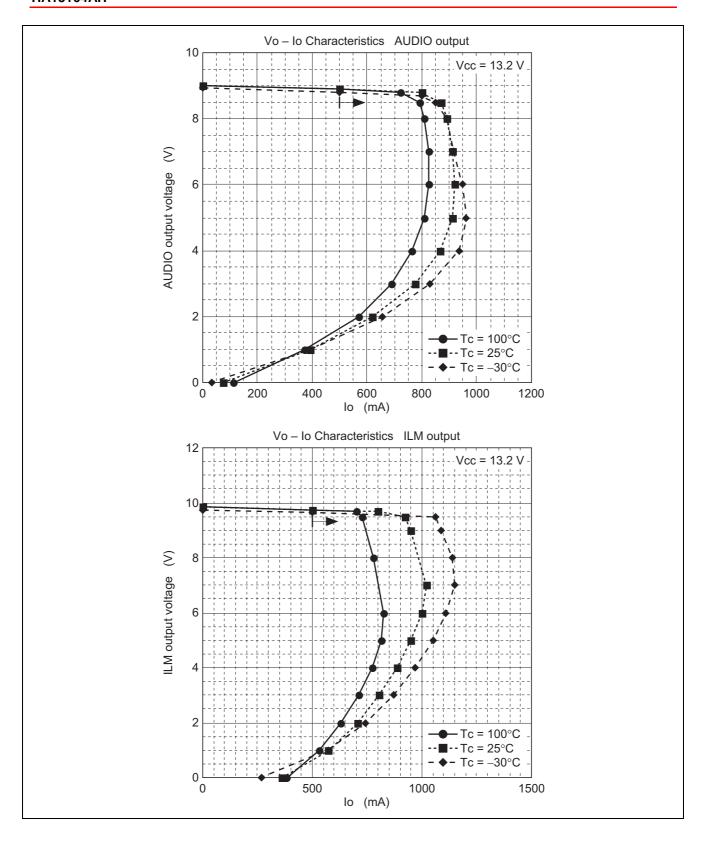


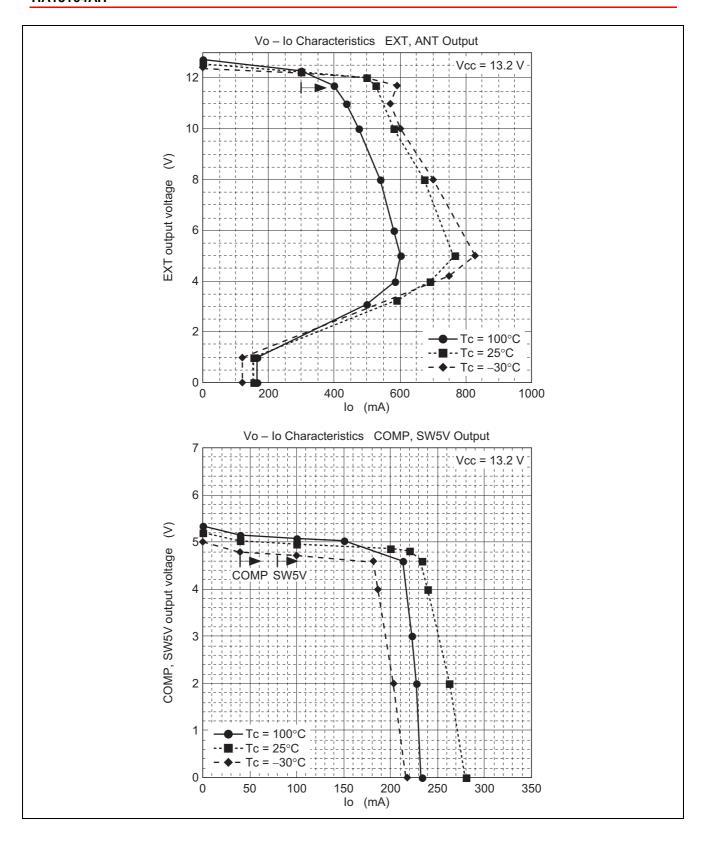
### **Main Characteristic**

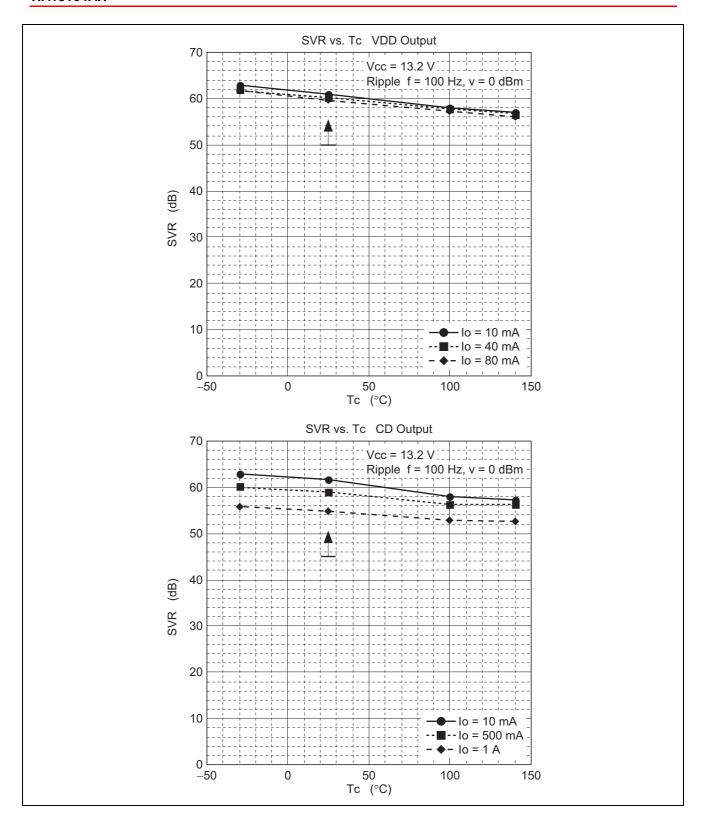


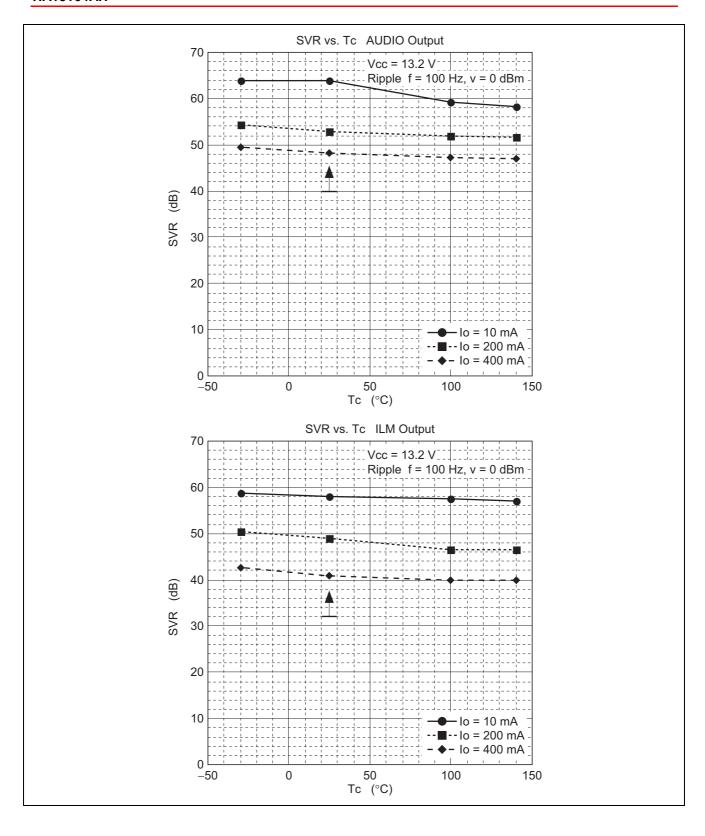




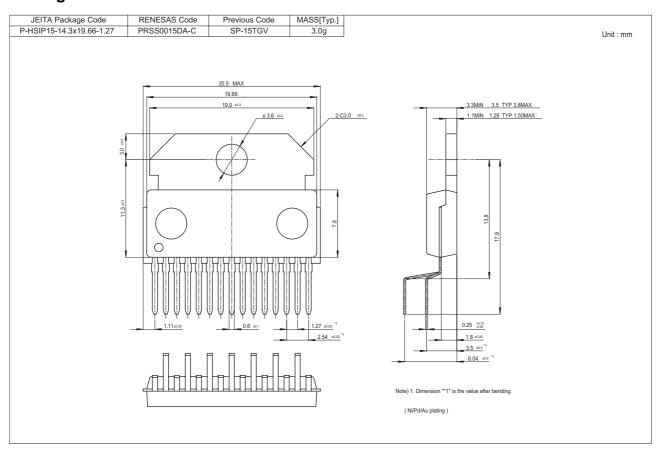








## **Package Dimensions**



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